UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,894,319 B2 DATED : May 17, 2005

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Page 1 of 1

INVENTOR(S) : Takashi Kobayashi et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page,

Item [54], Title, should read -- VERTICAL INSULATED GATE SEMICONDUCTOR DEVICE --

Item [30], Foreign Application Priority Data, should read

-- October 31, 2000 JP 2000-331840 November 28, 2000 JP 2000-361106 --

Signed and Sealed this

Nineteenth Day of July, 2005

JON W. DUDAS Director of the United States Patent and Trademark Office